





Berg is directed to monitoring an existing system for degradation and taking corrective action. Berg does not at all relate to the building of a time-sliced architecture, as required by the claimed invention. Since Berg does not relate to the building of a time-sliced architecture, it necessarily follows that Berg does not teach or suggest determining an optimal granularity, which is a feature of building time-slice architectures, or adjusting this optimal granularity based on a context switching overhead. Independent claims 1 and 7 are therefore patentable over Berg for at least these reasons.

Claims 2-5 depend on claim 1, and claims 8-11 depend on claim 7. These dependent claims are patentable over the applied references at least by virtue of their dependence on claims 1 and 7. Also, these claims recite further details of building of a time-sliced architecture. Since Berg is not related to the building of a time-sliced architecture, Berg also does not teach or suggest these further recited features. And thus the dependent claims are patentable over Berg for these additional reasons.

Claim 6 depends on claim 1 and claim 12 depends on claim 7. The Examiner additionally applies against these claims Cai as teaching a cache. While Cai may teach a cache, it fails to make up for the deficiencies of Berg as discussed above, and thus claims 6 and 12 are patentable at least by virtue of their dependence on claims 1 and 7, respectively.

The present invention, as outlined in claim 13, is directed to a time-sliced processor for use in a spread spectrum system. The processor includes a master control unit 110 including a time slot table 112 and a partial sums search table 114, a data cache 102 for receiving input data, and finger processing elements 104, 106, 108. Each finger processing element has a cache 122 for receiving data from the data cache 102, a data selector 124 connected to an output of the cache, a despreader 126 connected to an output of the data selector, and a symbol integrator 128 connected to an output of the despreader.

As stated above, Berg is directed to monitoring an existing system for degradation and taking corrective action. Berg does not at all relate to a time-sliced architectures, as required by the

claimed invention. Cai, which is directed to a-cache, does not make up for Berg's deficiencies. Moreover, contrary to the Examiner's assertions in the Office Action, Cai does not teach or suggest a master control unit or finger processing elements. The claimed master control unit 110 includes a time slot table 112 and a partial sums search table 114, features found in wireless communication systems and not suggested anywhere in Cai's cache system. Thus, claim 13 is patentable over the applied references for at least this reason.

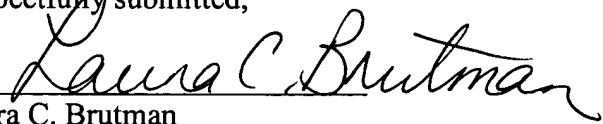
Cai also does not suggest finger processing elements, another feature of wireless communication systems. A finger processing element is an individual channel of a wireless rake receiver, which combines the outputs of the finger processing elements to form a stronger received signal. As recited, each finger processing element has not only a cache 122 and a data selector 124, but also a despreader 126 and a symbol integrator 128, which are also elements of wireless communication systems and not suggested anywhere in the cache system of Cai. Thus, claim 13 is patentable over the applied references for at least these additional reasons.

In view of the above amendment, Applicant believes the pending application is in condition for allowance.

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Respectfully submitted,

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